

# ACKNOWLEDGEMENT RECEIPT

Electronic Version

Stylesheet Version v01

## Title of Invention

STRUCTURE AND METHOD OF FORMING A BIPOLAR  
TRANSISTOR HAVING A VOID BETWEEN EMITTER AND  
EXTRINSIC BASE

Submission Type : Utility Patent Filing

Application Number:

10/708563



EFS ID:

56949

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First Named Applicant: Rama Divakaruni

Attorney Docket Number: FIS920030414US1

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File Listing:

| Doc. Name        | File Name                      | Size (Bytes) | Date Produced (yyyymmdd) |
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| us-request       | FIS920030414US1-usrequ.xml     | 3820         | 2004-03-11               |
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| us-ids           | FIS920030414US1-usidst.xml     | 1797         | 2004-03-11               |
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| us-ids           | us-ids.xml                     | 12026        | 2004-03-11               |
| us-assignment    | FIS920030414US1-usassn.xml     | 2382         | 2004-03-11               |
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| us-assignment    | FIS920030414US1Assignment2.tif | 12018        | 2004-03-11               |
| us-assignment    | FIS920030414US1Assignment3.tif | 9727         | 2004-03-11               |
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| application-body | us-application-body.xml        | 83497        | 2004-03-11               |
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